

A Report on
National Webinar
FULL CUSTOM VLSI CIRCUIT DESIGN USING ELECTRIC EDA TOOL
(15th July 2020)



Resource Person: DR. HARI PRASAD NAIK BHATTU

Associate Professor, Department of ECE,

Nalla Narasimha Reddy Education Society's Group of Institutions, Hyderabad

Dr.B.Hari Prasad Naik serves as Associate Professor in Nalla Narasimha Reddy Education Society's Group of Institutions. He has 12 years of experience in Teaching and Research. He earned his PhD Degree with VLSI as specialization from the University College of Engineering, Osmania University, Hyderabad in the year 2019. He obtained Post -Graduation Degree, M.E. in Digital System Engineering specialization from the University college of Engineering, OU in the year 2008. Graduated in Electronics and Communication Engineering from Anurag Engineering College, JNTUH, 2005. He has vast knowledge in the domains of VLSI i.e. semi-custom and full custom IC design. He has good computer proficiency in EDA Tools such as Cadence, Mentor Graphics, Advanced Design System, HFSS, Xilinx Vivado etc.

In his lecture, speaker Dr.B.Hari Prasad Naik, covered aspects with regard to VLSI Design Automation design flow. He also elaborated the purpose and need of automation of circuit design using various EDA software tools. Here, in this webinar, the complete design flow of open source software namely "Electric VLSI Design System" was demonstrated. The full custom design flow of VLSI circuits using Electric VLSI tool started with schematic, layout of various logic blocks. Finally, concluded the design flow with layout vs schematic comparison of the logic designs with simulation.


Head of the Department
Head of the Department

Electronics & Communication Engineering
Nalla Narasimha Reddy Education Society's
Group of Institutions - Integrated Campus
Chowdareguda(VIII), Ghatkesar (Mdl), R.R. Dist 500 088